Clearly indicate your answers please.

1. x86 in-line assembly instructions for the GCC Compiler follow which syntax standard?
   A. Intel
   B. PG&E
   C. AT&T
   D. NASM

2. This statement: `asm ("movl %%eax, %%ebx");`
   A. will move ebx's content into eax.
   B. will move eax's content into ebx.
   C. will move bx's content to ax.
   D. will set registers eax and ebx to 1.

3. Write an in-line assembly statement that will set the eax register to 10.
   ```
   asm ("movl $10, %%eax");
   ```

4. What is a clobber list?
   A. Unused registers.
   B. Registers that are available for use.
   C. Registers that should not be used for in-line code.
   D. Registers used by your own in-line code.

5. What modifier tells the GCC compiler to not attempt any optimization of your in-line assembly code block?
   A. volatile
   B. extern
   C. inline
   D. static

6. The x86 bit-shift instructions accept a count operand indicating how many steps the bits should be shifted. The count value is masked to 5-bits, which means that the maximum shift steps would be...
   A. 5
   B. 25
   C. 31
   D. 32